

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
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Gerwin PREISINGER et al.)	Group Art Unit: Unassigned
)	
Application No.: Unassigned)	Examiner: Unassigned
)	
Filed: February 5, 2004)	Confirmation No.: Unassigned
)	
For: DEVICE FOR PROTECTING A BEARING OF)	
AN ELECTRICAL MACHINE FROM A)	
DESTRUCTIVE CURRENT)	

FIRST INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.56, the following information is brought to the Examiner's attention.

Toshihisa SHIMIZU et al., "High Frequency Leakage Current Reduction Based on a Common-Mode Voltage Compensation Circuit," *IEEE*, pages 1961-1967, 1996.

To assist the Examiner in entering the foregoing information into the record, the above-noted document is are cited on the attached form PTO-1449 and a copy of the document is enclosed.

The Examiner is respectfully requested to consider the foregoing information and to acknowledge such consideration by initialling, signing and returning the enclosed additional copy of form PTO-1449.

Respectfully submitted,
BURNS, DOANE, SWECKER, & MATHIS, L.L.P.

Date: February 5, 2004

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